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By: Manfred Beck

Date: July 21, 2003

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Michael Jacob et al.
Applic. No. : 10/053,970
Filed : January 22, 2002
Title : Test Circuit for an Analog Measurement of Bit
Line Signals of Ferroelectric Memory Cells
Examiner : Viet Q. Nguyen
Group Art Unit : 2818

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A M E N D M E N T

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

S i r :

Responsive to the Office action dated May 8, 2003 kindly amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of claims which begin on page 2 of this paper.

Remarks/Arguments begin on page 12 of this paper.